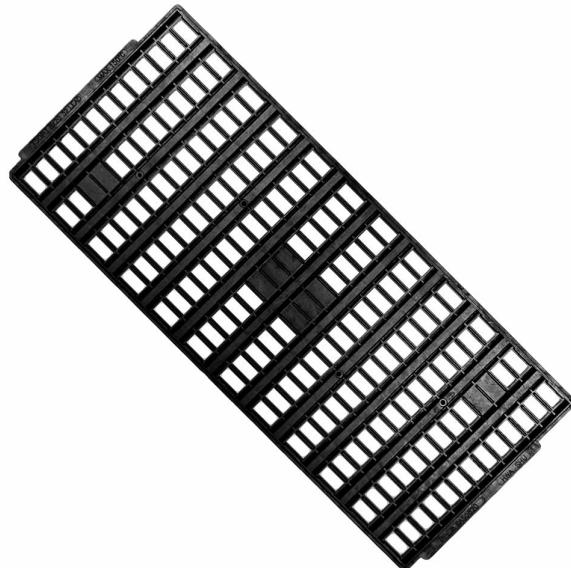


**PRODUCT**

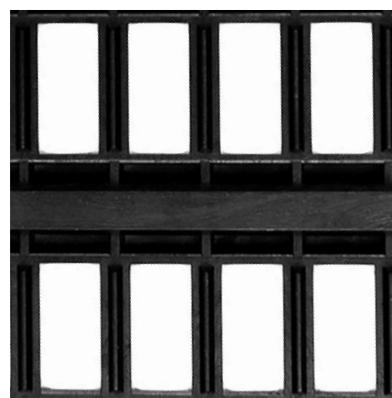
# Waffle Trays - TSOP I

## TECHNICAL DATASHEET


**DESCRIPTION**

Manufactured to meet stringent JEDEC global standards, the waffle tray design enables integrated circuits to be handled safely and securely during the various stages of manufacturing, testing, and shipping.

All JEDEC waffle trays have the same “X” and “Y” dimensions and are easily stacked for storage and transportation.

**CELL STYLE**

**APPLICATIONS**

- Assembly and testing
- Shipping and storage
- Manufacturing processes
- Pick and place automation

PRODUCT CODE	CELL SIZE (mm)	MATRIX	USABLE CELLS	MAX HEAT (°C)	QUANTITY
030-0053	8 X 20	13 x 12	146	150	Each

To request a quotation or for more information, please call **+44 (0)1473 836200**  
 email [info@antistat.co.uk](mailto:info@antistat.co.uk) or visit [www.antistat.co.uk](http://www.antistat.co.uk)

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